

	Type	L #	Hits	Search Text	DBs
1	IS&R	L1	1000	(327/284,285,288).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
2	IS&R	L2	1115	(327/261).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
3	BRS	L3	19949	delay.clm.	US-PGPUB
4	BRS	L4	1731	3 and transistor.clm.	US-PGPUB
5	BRS	L5	630	4 and inverter\$1.clm.	US-PGPUB
6	BRS	L6	119	5 and threshold.clm.	US-PGPUB
7	BRS	L7	1341	delay.clm. and transistor.clm. and threshold.clm.	US-PGPUB; USPAT
8	BRS	L8	434	7 and inverter.clm.	US-PGPUB; USPAT
9	BRS	L9	315	7 and inverter.clm.	USPAT
10	BRS	L10	8358	delay.ti.	US-PGPUB; USPAT
11	BRS	L11	1944	10 and inverter\$1	US-PGPUB; USPAT
12	BRS	L12	1287	11 and transistor\$1	US-PGPUB; USPAT
13	BRS	L13	1080	12 and (threshold size width length)	US-PGPUB; USPAT
14	BRS	L14	566	13 and transistor\$1.clm.	US-PGPUB; USPAT
15	BRS	L15	499	14 not (6 9)	US-PGPUB; USPAT